

Course Syllabus

[Jump to Today](#)

 [Edit](#)

Course Description

This course covers modern computer architecture, including branch prediction, out-of-order instruction execution, cache optimizations, multi-level caches, memory and storage, cache coherence and consistency, and multi- and many-core processors.

Professor and TAs

Professor

Milos Prvulovic

Office hours: Tuesdays 11:05am to 11:55am in Atlanta (US Eastern time), we will be using BlueJeans, and you can use this [link](https://bluejeans.com/218861856) [.\(https://bluejeans.com/218861856\)](https://bluejeans.com/218861856) to join the meetings.

TAs

TBD

Prerequisites

Undergraduate computer architecture course that covers basic computer organization; working knowledge of topics such as instruction sets, pipelining, etc. For the course project, you will also need to be familiar with C/C++, Linux, and be comfortable making modifications to large programs.

If you answer “no” to any of the following questions, it may be beneficial to refresh your knowledge of the prerequisite material prior to taking CS 6290:

- Have you taken a computer organization course before?
- Are you familiar with at least one RISC instruction set and would you feel comfortable reading and writing small assembler programs?
- Are you familiar with basic computer architecture concepts, such as pipelines and caches?
- Are you familiar with C/C++ and would you be comfortable writing and/or modifying 100+ lines of code in a program that has over 100,000 lines of code?
- Are you comfortable with, or even excited about, learning about how real processors work and using simulation to see how changes in processor design affect its performance?
- Have you successfully completed the HPCA0 "course" at Udacity?

Textbook

There are no required readings. When appropriate, additional class materials will be available as instructor notes that are associated with the video lectures. Although we do not require, and do not officially recommend, a textbook, a useful textbook for this course is “Computer Architecture: A

Quantitative Approach” by John L. Hennessy and David A. Patterson. A recent edition should work, but editions 1-4 put less emphasis on multi-core topics than our course does.

Grading

The grade is determined by your performance on projects and exams. You will receive these grades through Canvas. The projects and exams will count toward the final grade as follows:

- Projects (50% of overall grade): You will be given four projects, each requiring more work than the previous one. Each project is to be completed individually or in two-student teams, as specified in each project assignment.
 - Project 0 (5% of overall grade)
 - Project 1 (10% of overall grade)
 - Project 2 (15% of overall grade)
 - Project 3 (20% of overall grade)
- Exams (50% of overall grade)
 - Midterm (20% of overall grade)
 - Final (30% of overall grade), it **does** include questions about material covered in the Midterm

The plan is to assign final (letter) grades based on your total score, with 90% and above earning an A, 80% and above earning a B, etc. If this results in too few As, we may decide to lower the thresholds somewhat, or to use some sort of a curve - the final decision whether and what to do in this regard is up to the instructor.

There will be **no make-up assignments**, so if you need a particular grade plan to perform accordingly on projects and exams. Once a homework, project, or exam is over and graded, the only way the score on that assignment or exam will be changed is if a legitimate mistake in grading has been made. Due to the large number of students in this class, assignment and exam re-grades can only be requested during 14 days that follow the release of scores from that assignment/exam. When requesting a re-grade, keep in mind **that the entire submitted project/exam will be regraded**, so a request for a regrade may result in a net loss of points.

The grade in this class will be based solely on demonstrated performance. No grade will ever be changed because the student **needs** a better grade to stay in the program, to keep a fellowship, to get a job, or any other reason. If you believe you need some particular grade in this class, the only way to get that grade is to earn it on projects and exams.

Emergencies and Late Policy

No late assignments or exams will be accepted unless we are advised to do so by the Dean of Students. Please contact the office of the Dean of Students with health emergencies, family emergencies, personal disabilities, or other significant events. The Dean’s office is equipped to verify these exceptions better than us, and provides a level of uniformity across courses on how emergencies are handled.

Academic Integrity

All Georgia Tech students are expected to uphold the [Georgia Tech Honor Code](http://osi.gatech.edu/content/honor-code) (<http://osi.gatech.edu/content/honor-code>). **You should read it (including the Graduate Addendum)!** We take cheating **very** seriously. Note that all Georgia Tech faculty (including the instructor for this course) are **required** to report cases of academic dishonesty to the Dean of Students' office at Georgia Tech.












Class Schedule

This schedule lists important dates (exams, project release and due dates, etc.). The white-background items show the recommended schedule for completing lessons. Of course you can complete lessons at a faster pace, but projects and exams are timed assuming that you will take the lectures according to the provided schedule - so do not fall behind!

Week	Dates	Topics
1	May 11-15	Introduction, Metrics and Evaluation; Pipelining Review; Branch Prediction
	May 11	Project 0 Released
2	May 18-22	Predication; ILP and Instruction Scheduling
	May 18	Project 1 Released
	May 20	Project 0 Due at midnight AOE (GMT-12)
3	May 25	Official School Holiday
	May 26-29	ROB; Memory Ordering; Compiler ILP and VLIW
	May 29	Project 2 Released
4	Jun 1-5	Cache Review and Virtual Memory
	Jun 7	Project 1 Due at midnight AOE (GMT-12)
5	Jun 8-12	Advanced Caches, Memory
	Jun 12-14	Midterm Exam (2-hour proctored exam)
6	Jun 15-19	Storage and Fault Tolerance
	Jun 15	Project 3 Released
	Jun 21	Project 2 Due at midnight AOE (GMT-12)
7	Jun 22-26	Multi-Processing; Begin Cache Coherence
	Jun 27	Last day to drop course with "W" grade
8	Jun 29- Jul 1	Finish Cache Coherence
	Jul 2-3	Official School Holiday
9	Jul 6-10	Synchronization; Begin Memory Consistency
10	Jul 13-17	Finish Memory Consistency; Many-Core

Week	Dates	Topics
	Jul 19	Project 3 Due at midnight AOE (GMT-12)
15	July 20-22	Catch-Up and Prepare for Exams
	July 24-26	Final Exam (3-hour proctored exam)

Course Summary:

Date	Details	
Thu May 21, 2020	 Project 0 (https://gatech.instructure.com/courses/128866/assignments/421324)	due by 8am
Mon Jun 8, 2020	 Project 1 (https://gatech.instructure.com/courses/128866/assignments/421326)	due by 8am
Tue Jun 16, 2020	 Midterm (https://gatech.instructure.com/courses/128866/assignments/421316)	due by 10am
Mon Jun 22, 2020	 Project 2 (https://gatech.instructure.com/courses/128866/assignments/421328)	due by 8am
Mon Jul 20, 2020	 Project 3 (https://gatech.instructure.com/courses/128866/assignments/421330)	due by 8am
Tue Jul 28, 2020	 Final (https://gatech.instructure.com/courses/128866/assignments/421314)	due by 11am
	 [Proctoring]Final (https://gatech.instructure.com/courses/128866/assignments/421318)	
	 [Proctoring]Final (https://gatech.instructure.com/courses/128866/assignments/467166)	
	 [Proctoring]Midterm (https://gatech.instructure.com/courses/128866/assignments/421320)	
	 [Proctoring]Midterm (https://gatech.instructure.com/courses/128866/assignments/467164)	
	 [Proctoring]Proctortrack On-boarding Quiz (https://gatech.instructure.com/courses/128866/assignments/421322)	